



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED

98 AUG 11 AM 10:39

GROUP 2700

APPLICANT: Subhash C. Roy et al.

SERIAL NO.: 09/064,474

FILED: April 22, 1998

FOR: Real Time Debugger
Interface for Embedded
Systems



GROUP ART UNIT: 2764

EXAMINER:

ATT'Y DOCKET: TRA-040

Honorable Commissioner of Patents
and Trademarks
Washington, D.C. 20231

Sir:

I hereby certify that this correspondence is being deposited with
the United States Postal Service as first class mail in an
envelope addressed to : Commissioner of Patents and
Trademarks, Washington, D.C. 20231 on AUG 4, 1998

David P. Gordon
David P. Gordon
Reg. No. 29,996

Aug 4, 1998
Date

SUBMITTAL OF DOCUMENTS PURSUANT TO DUTY OF DISCLOSURE

Pursuant to applicant's duty of disclosure 37 CFR Section 1.56, enclosed is a completed form PTOL-1449 as well as copies of the cited documents which relate to the above-referenced patent application.

U.S. Patent #5,491,793 to Somasundaram et al. discloses a debug support in a processor chip.

U.S. Patent #5,513,346 to Satagopan et al. discloses error condition detector for handling interrupt in integrated circuits having multiple processors.

U.S. Patent #5,640,542 to Whitsel discloses on-chip in-circuit-emulator memory mapping and breakpoint register modules.

Motorola MPC860 User's Manual discloses a development system interface.

The listed documents are brought to the Examiner's attention because they are known to the applicant and/or the applicant's attorney and may be considered by the Examiner to be material to his/her examination. This listing should not be construed as representation that a search has been made or that no better art exists. No inference should be made that the documents are in fact material merely because they are referenced herein. Moreover, no representation is made that the brief descriptions of the references herein necessarily describe the most material